



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of

ITO

Application Number: 10/770,479

Filed: February 4, 2004

**For: INTEGRATED CIRCUIT, INTEGRATED
CIRCUIT DESIGN METHOD AND
HARDWARE DESCRIPTION GENERATION
METHOD TO GENERATE HARDWARE
BEHAVIOR DESCRIPTION OF INTEGRATED
CIRCUIT**

Attorney Docket No. NITT.0189

**Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231**

Art Unit 2825

**Examiner
Thompson, Annette M.**

PETITION FOR EXTENSION OF TIME

Sir:

Applicants request that the time for taking action in this case be extended pursuant to 37 C.F.R. § 1.136(a) for **one (1) month**. The fee set in 37 C.F.R. § 1.17 for the extension of time is **\$120.00**, which is being submitted herewith.

Please charge any additional fee required for this extension of time to Deposit Account 08-1480. A duplicate copy of this paper is enclosed.

Respectfully submitted,

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120.00 OP

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September 19, 2005

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